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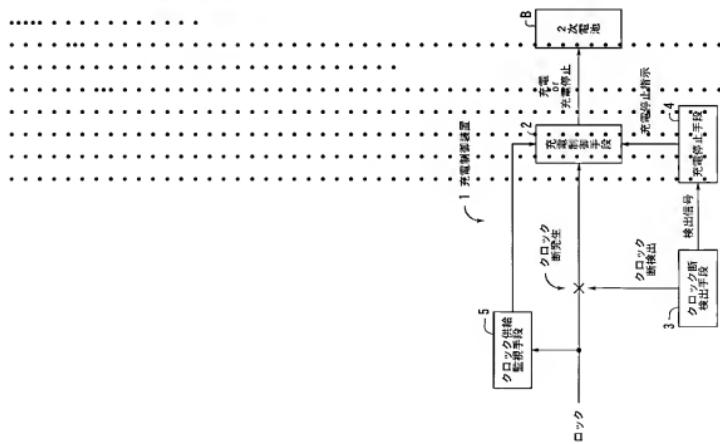
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(54)【発明の名称】充電制御装置



(2)

1

2

10

100

1000

10000

100000



(4)

5

6

\*10\*

\*20\*

\*30\*

\*40\*

\*50\*

(5)

7

8

\*10\*

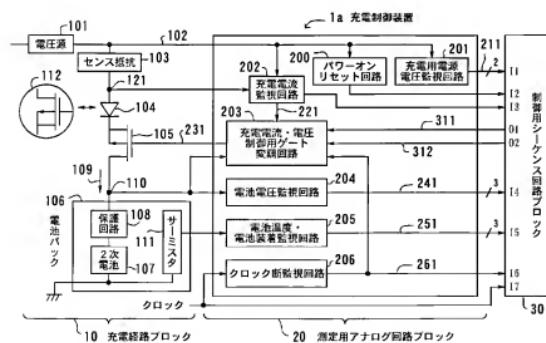
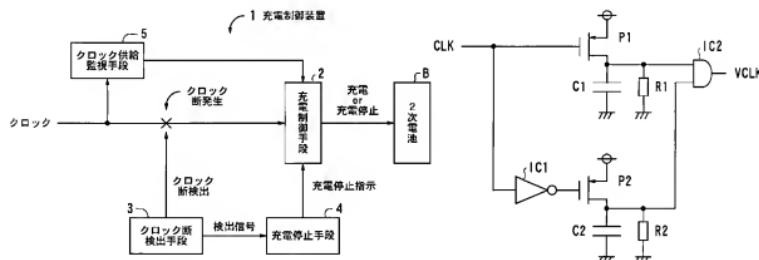
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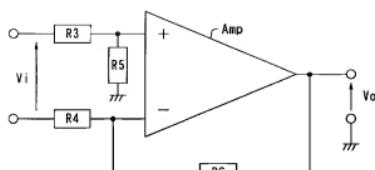
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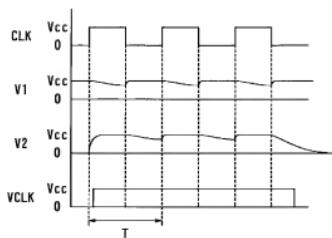
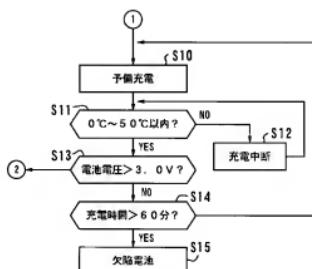
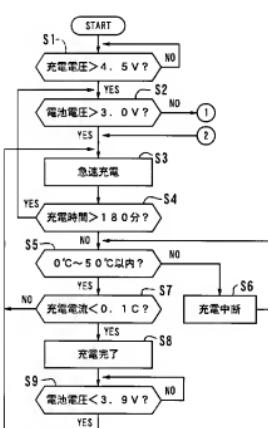
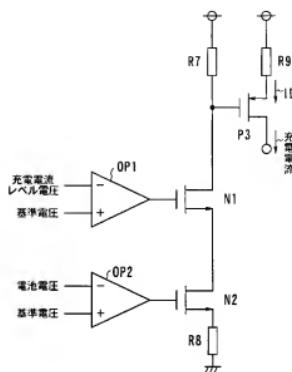


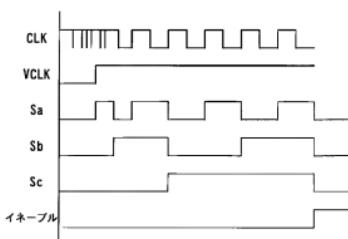
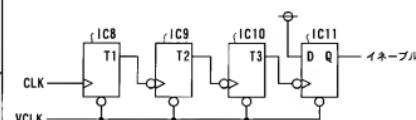
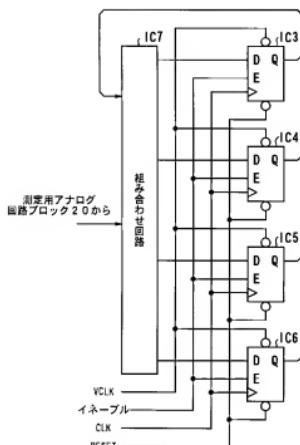
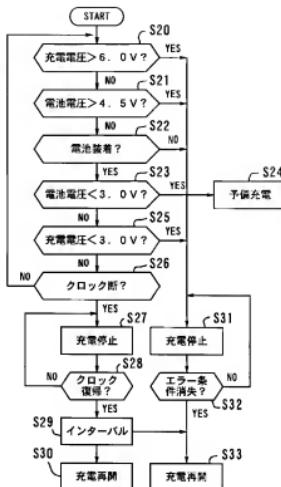
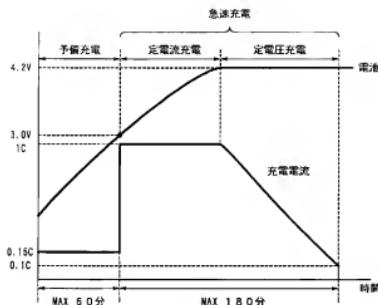


(B)



$$V_o = \frac{R_6}{R_4} V_i$$





# PATENT ABSTRACTS OF JAPAN

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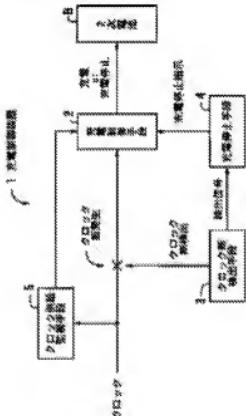
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(21)Application number : 2000-180827 (71)Applicant : FUJI ELECTRIC CO LTD

(22)Date of filing : 16.06.2000 (72)Inventor : YOSHIDA YUTAKA

## (54) CHARGE CONTROLLER



### (57)Abstract:

**PROBLEM TO BE SOLVED:** To prevent malfunctions in clock interruption for the enhancement of the safety and quality of charging operation.

**SOLUTION:** A charge controlling means 2 logically controls charging of a secondary battery B. A clock interruption detecting means 3 detects any clock interruption in clocks inputted to the charge controlling means 2 and generates a detection signal if any clock interruption occurs. A charge stopping means 4 stops charging operation based on the detection signal.